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\*\* CONTINUING DATA \*\*\*\*\*

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

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35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after allowance	Verified and Acknowledged	Examiner's Signature <i>[Signature]</i>	Initials <i>GL</i>	

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TITLE  
 Semiconductor wafer treatment method, semiconductor wafer inspection method, semiconductor device  
 development method and semiconductor wafer treatment apparatus

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